

an electrode pattern formed on the second substrate;
a seal pattern formed peripherally to said active area completely surrounding the electrode pattern, and contacting said first substrate and said second substrate;
a liquid crystal layer between said first substrate and said second substrate, and on the active area,

wherein said second substrate comprises:

data lines and gate lines arranged in a matrix shape to define pixel areas;

a TFT at an intersection of a data line and a gate line;

a protective film on the TFT; and

a pixel electrode on said protective film.

wherein the electrode pattern is between said seal pattern and said protective film.